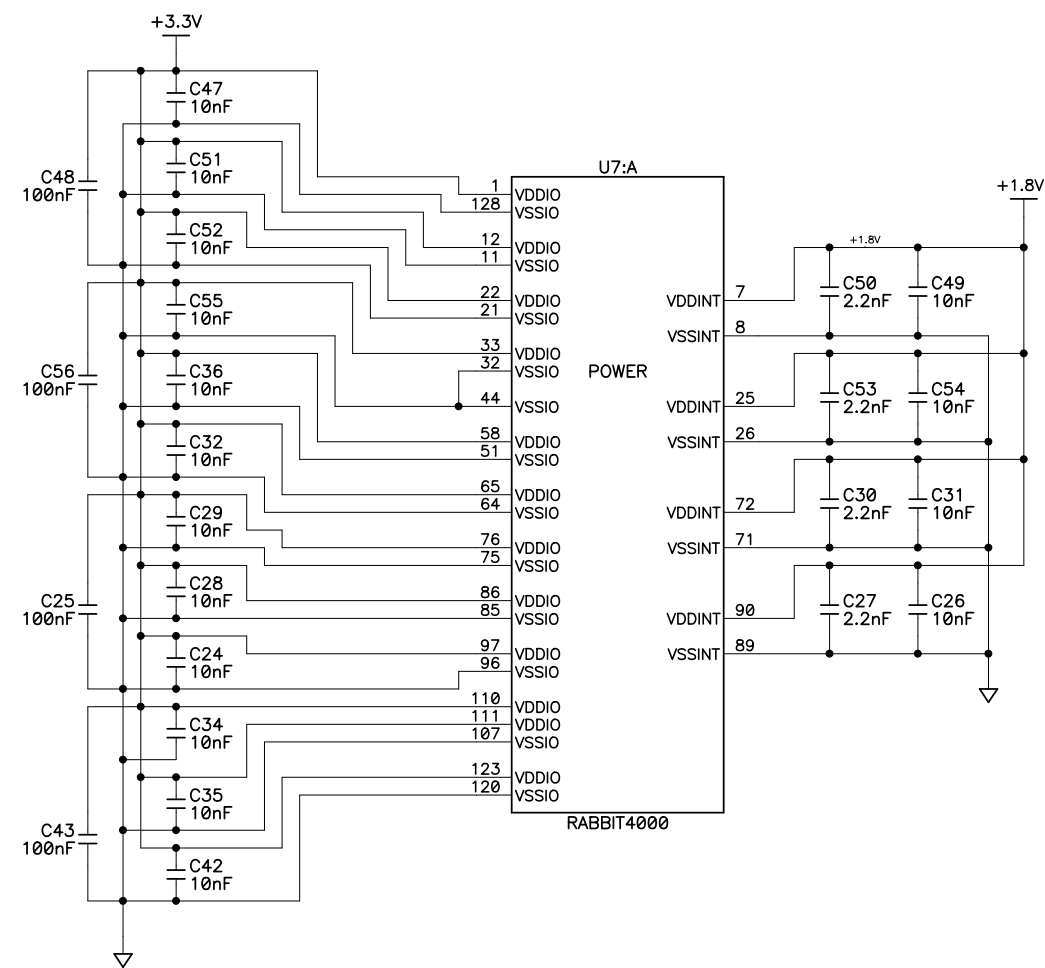
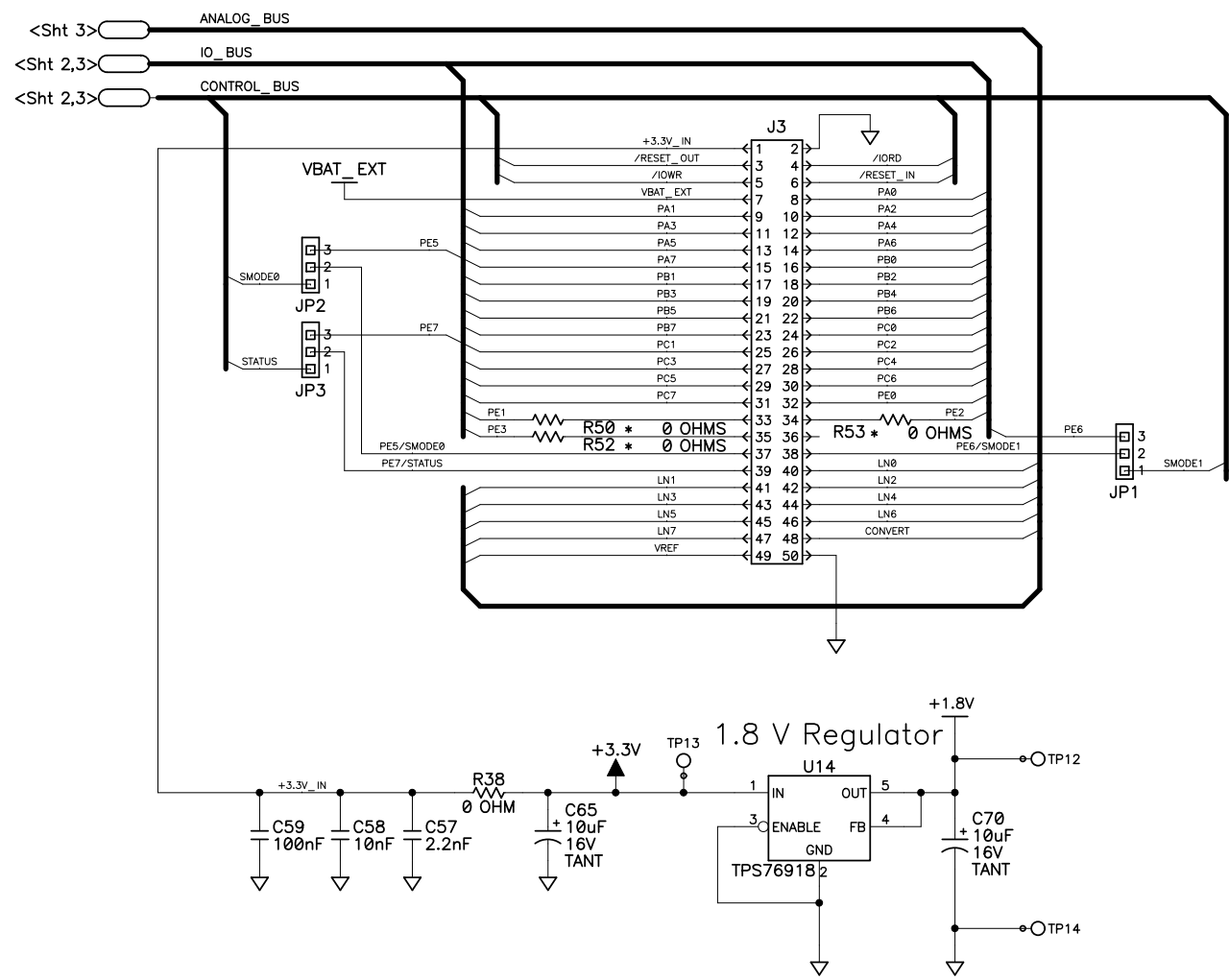


REVISION HISTORY			REVISION APPROVAL			
REV	ECO	DESCRIPTION	PROJECT ENGINEER	APPROVAL DATE	DOCUMENT CONTROL	APPROVAL DATE
A	E14434	INITIAL RELEASE	JC	4/28/06	KF	4/28/06
B	E14579	IMPROVE ANALOG AND NAND FLASH CONTROL.	JC	6/7/06	KF	5/31/06
C	E15410	ADD RCM4020 STUFFING TABLE	JC	11/28/06		
D	15587	UPDATE SRAM AND FLASH PIN OPTION TABLES, UPDATE C69, ADDED R58, R59, OPTION FOR C75 AND C60	TCP	02/26/07		
E	15633	ADD U13, R60-R62 FOR RCM4020 OPTIONS. UPDATE C69, ADD TP16, ADD C76	TCP			
F	000563	RENAME RCM4020 TO RCM4050	TCP	02/26/07		
G	001150	UPDATE STUFFING TABLE TO REFLECT THE CORRECT CONFIGURATIONS TO CORRESPOND WITH 20-175-0406 PCB.	TCP	5/5/09		

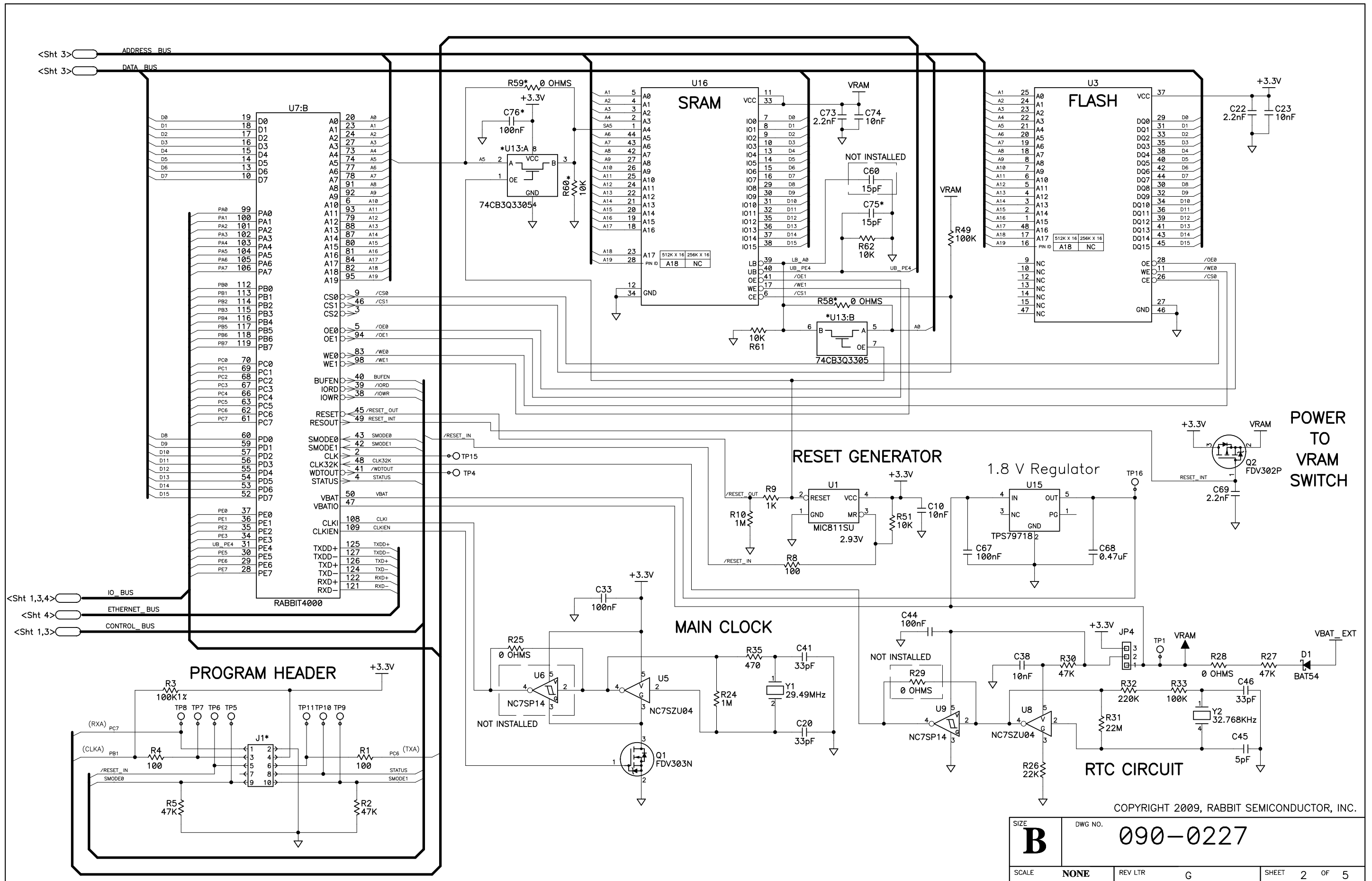
CORE HEADER

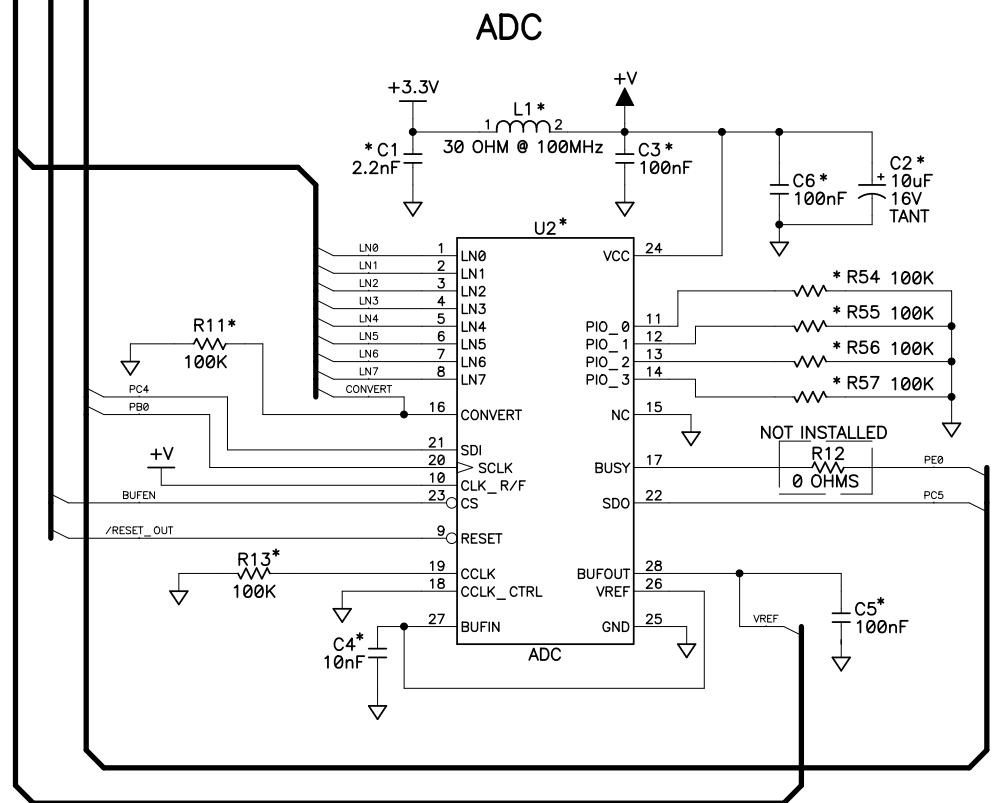
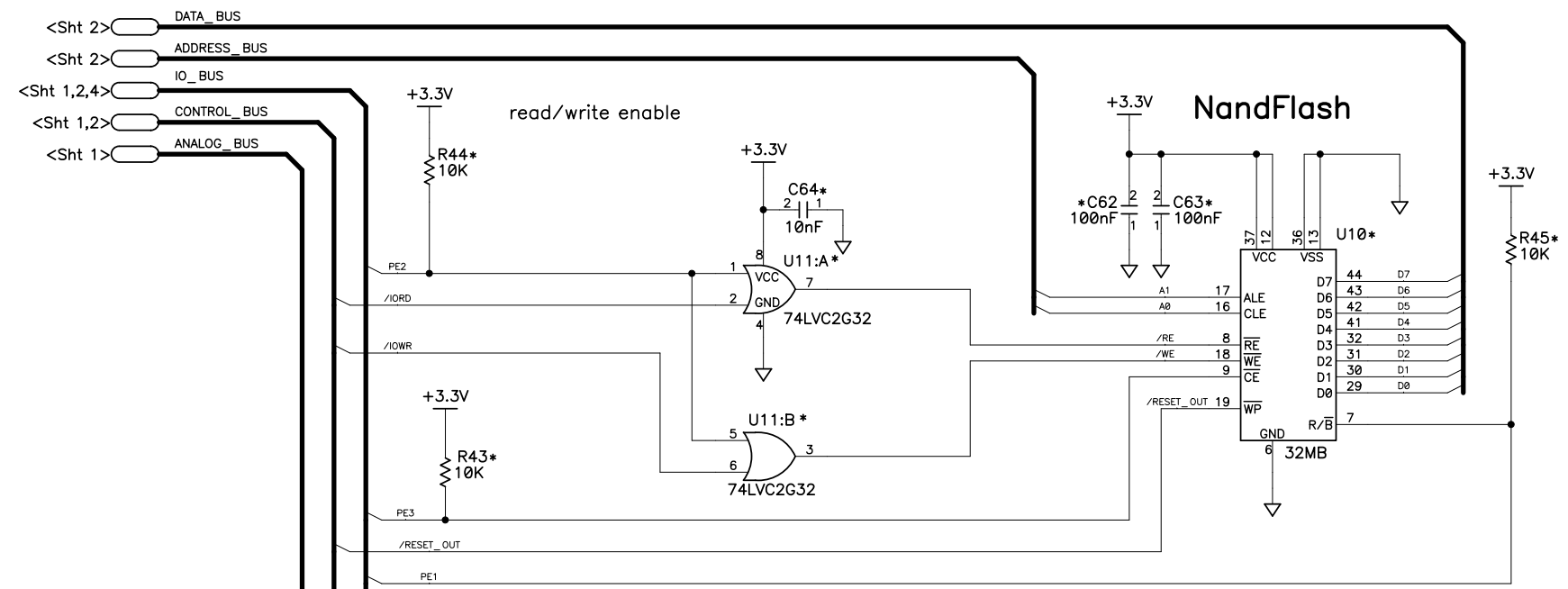


- NOTES: UNLESS OTHERWISE SPECIFIED;
- ALL RESISTOR VALUES ARE IN OHMS, 1/16W, 1%
 - THE ORIGIN SOURCE OF A VOLTAGE IS REPRESENTED BY (VCC), AND ALL REFERENCES TO THAT VOLTAGE ARE REPRESENTED BY (VCC).
 - COMPONENT VALUES SHOWN WITH AN ASTERISK (*) FOLLOWING THE VALUE, MAY HAVE DIFFERENT VALUES, OR MAY NOT BE STUFFED. SEE STUFFING CHART FOR CLARIFICATION.

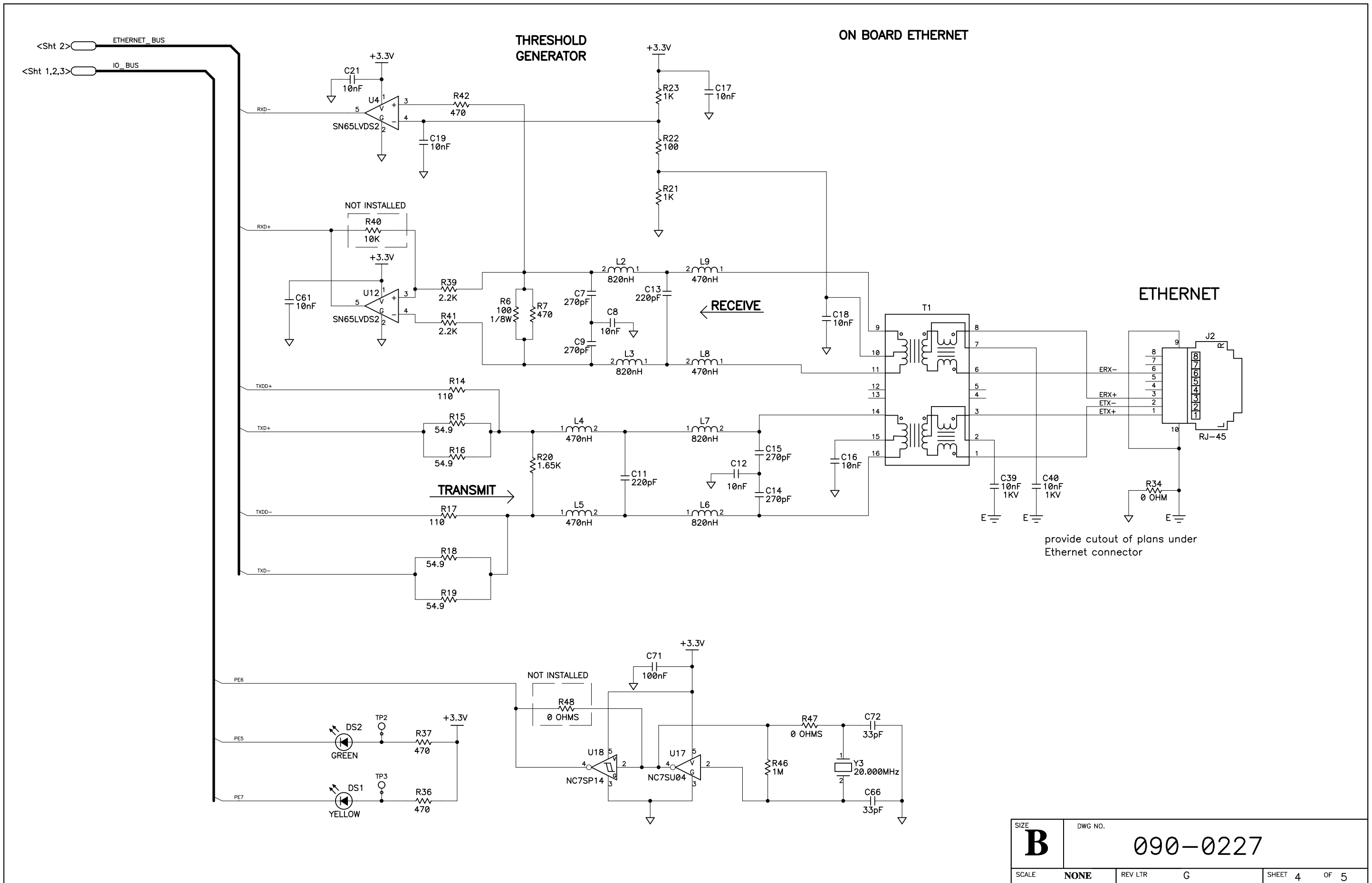
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APPEND THE FOLLOWING DOCUMENTS WHEN CHANGING THIS DOCUMENT:		DRAWING CONTENT:		TITLE	
		DRAWN BY: (INITIAL RELEASE) J CAMPBELL	2/3/06	SCHEMATIC DIAGRAM RCM4000	
		REVISED BY: TCP	3/27/08		
APPROVALS: INITIAL RELEASE				RABBIT SEMICONDUCTOR www.rabbit.com	
		PROJECT ENGINEER: J CAMPBELL	2/3/06		
		ENGINEERING MANAGER: J BAUMERT			
		SIGNATURES	DATE	SIZE B	DWG NO. 090-0227
				SCALE NONE	RELEASE DATE
				SHEET	1 OF 5





SIZE B	DWG NO. 090-0227
SCALE NONE	REV LTR G
SHEET 3 OF 5	



SIZE B	DWG NO. 090-0227
SCALE NONE	REV LTR G
SHEET 4 OF 5	

RCM4000 STANDARD CONFIGURATION OPTIONS			
OPTION	RCM4000	RCM4010	RCM4050
SRAM	512KB 55ns	512KB 55ns	1MB
FLASH	512KB	512KB	1MB
ADC	12 BIT	NONE	NONE
ALT SIGNALS	PROG SIGNALS	PROG SIGNALS	PROG SIGNALS
NAND FLASH	32M	NONE	32M
ETHERNET	10BASE-T	10BASE-T	10BASE-T
RTC POWER	BATTERY BACKUP	BATTERY BACKUP	BATTERY BACKUP

OPTION: SRAM			
U16	512KB 55ns	1MB 55ns	
C75	NOT INSTALLED	15pF	
R58,R59	INSTALLED	NOT INSTALLED	
U13,R60,C76	NOT INSTALLED	INSTALLED	

OPTION: FLASH		
U3	512KB	1MB

OPTION: ADC			
PART	NONE	12 Bit	
U2	NOT INSTALLED	12 BIT INSTALLED	
C1,C2,C3,C4,C5,C6	NOT INSTALLED	INSTALLED	
L1	NOT INSTALLED	INSTALLED	
R11,R13,R54,R55,R56,R57	NOT INSTALLED	INSTALLED	
R12	NOT INSTALLED	NOT INSTALLED	

OPTION: ALT SIGNALS (SELECTS PIN 37-39 FUNCTION, PORTE[5..7] or SMODE0, SMODE1,STATUS)		
PARTS	PORTE[5..7]	PROG SIGNALS
JP1,JP2,JP3	0 Ohm (2-3)	0 Ohm (1-2)

OPTION: NAND FLASH			
PART	NONE	32M	
U10	NOT INSTALLED	32MB INSTALLED	
U11	NOT INSTALLED	INSTALLED	
C62,C63,C64	NOT INSTALLED	INSTALLED	
R43,R44,R45	NOT INSTALLED	INSTALLED	
R50,R52,R53	INSTALLED	NOT INSTALLED	

OPTION: ETHERNET		
PARTS	NONE	10BASE_T
ALL PARTS SHEET 4	NOT INSTALLED	INSTALLED

CLOCK CONFIGURATIONS	
PART	
R25	INSTALLED
U6	NOT INSTALLED
R29	NOT INSTALLED
U9	INSTALLED
R48	NOT INSTALLED
U18	INSTALLED

OPTION: RTC POWER		
PARTS	BATTERY BACKUP	NO BATTERY BACKUP
JP4	0 Ohm (1-2)	0 Ohm (2-3)

SIZE B	DWG NO. 090-0227
SCALE NONE	REV LTR G
SHEET 5	OF 5