Multifunction GPIO							
Pin Number	**BSDL Name**	**Signal Name**		**OD (mA)**	**I/O**	**Buf Type**	**Descriptions (four options)**
W5	gpio[0]	gpio[0]	pull up	2	I/O	BD2CUIF	Ser port B TxData / DMA ch 1 done / Timer 1 / GPIO 0
V6	gpio[1]	gpio[1]	pull up	2	I/O	BD2CUIF	Ser port B RxData / DMA ch 1 req / Ext IRQ 0 / GPIO 1
Y5	gpio[2]	gpio[2]	pull up	2	I/O	BD2CUIF	Ser port B RTS / Timer 0 / DMA ch 2 rd en / GPIO 2
W6	gpio[3]	gpio[3]	pull up	2	I/O	BD2CUIF	Ser port B CTS / 1284 periph ack / DMA ch 1 req / GPIO 3
V7	gpio[4]	gpio[4]	pull up	2	I/O	BD2CUIF	Ser port B DTR / 1284 periph busy / DMA ch 1 done / GPIO 4
Y6	gpio[5]	gpio[5]	pull up	2	I/O	BD2CUIF	Ser port B DSR / 1284 periph error / DMA ch 1 rd en / GPIO 5
W7	gpio[6]	gpio[6]	pull up	2	I/O	BD2CUIF	Ser port B RxClk / 1284 paper jam / Timer 7 / GPIO 6
Y7	gpio[7]	gpio[7]	pull up	2	I/O	BD2CUIF	Ser port B TxClk / DMA ch 1 rd en / Ext IRQ 1 / GPIO 7
V8	gpio[8]	gpio[8]	pull up	2	I/O	BD2CUIF	Ser port A TxData / reserved / reserved / GPIO 8
W8	gpio[9]	gpio[9]	pull up	2	I/O	BD2CUIF	Ser port A RxData / reserved / reserved / GPIO 9
Y8	gpio[10]	gpio[10]	pull up	2	I/O	BD2CUIF	Ser port A RTS / reserved / PWM ch 0 / GPIO 10
V9	gpio[11]	gpio[11]	pull up	2	I/O	BD2CUIF	Ser port A CTS / Ext IRQ 2 / Timer 0 / GPIO 11
W9	gpio[12]	gpio[12]	pull up	2	I/O	BD2CUIF	Ser port A DTR / reserved / PWM ch 1 / GPIO 12
Y9	gpio[13]	gpio[13]	pull up	2	I/O	BD2CUIF	Ser port A DSR / Ext IRQ 0 / PWM ch 2 / GPIO 13
W10	gpio[14]	gpio[14]	pull up	2	I/O	BD2CUIF	Ser port A RxClk / Timer 1 / PWM ch 3 / GPIO 14
V10	gpio[15]	gpio[15]	pull up	2	I/O	BD2CUIF	Ser port A TxClk / Timer 2 / LCD clock in / GPIO 15
Y10	gpio[16]	gpio[16]	pull up	2	I/O	BD2CUIF	USB overcurrent / 1284 paper jam / reserved / GPIO 16
Y11	gpio[17]	gpio[17]	pull up	2	I/O	BD2CUIF	USB power relay / reserved / reserved / GPIO 17
V11	gpio[18]	gpio[18]	pull up	4	I/O	BD4CUIF	Ethernet CAM reject / LCD power enable / Ext IRQ 3 / GPIO 18
W11	gpio[19]	gpio[19]	pull up	4	I/O	BD4CUIF	Ethernet CAM Req / LCD line-horz sync / DMA ch 2 rd en / GPIO 19
Y12	gpio[20]	gpio[20]	pull up	8	I/O	BD8CUIF	Ser port C DTR / LCD clock / reserved / GPIO 20
W12	gpio[21]	gpio[21]	pull up	4	I/O	BD4CUIF	Ser port C DSR / LCD frame pulse-vert / reserved / GPIO 21
V12	gpio[22]	gpio[22]	pull up	4	I/O	BD4CUIF	Ser port C RxClk / LCD AC bias - data enable / reserved / GPIO 22
Y13	gpio[23]	gpio[23]	pull up	4	I/O	BD4CUIF	Ser port C TxClk / LCD line end / reserved / GPIO 23
W13	gpio[24]	gpio[24]	pull up	4	I/O	BD4CUIF	Ser port D DTR / LCD data bit 0 / reserved / GPIO 24
V13	gpio[25]	gpio[25]	pull up	4	I/O	BD4CUIF	Ser port D DSR / LCD data bit 1 / reserved / GPIO 25
Y14	gpio[26]	gpio[26]	pull up	4	I/O	BD4CUIF	Ser port D RxClk / LCD data bit 2 / Timer 3 / GPIO 26
W14	gpio[27]	gpio[27]	pull up	4	I/O	BD4CUIF	Ser port D TxClk / LCD data bit 3 / Timer 4 / GPIO 27
Y15	gpio[28]	gpio[28]	pull up	4	I/O	BD4CUIF	Ext IRQ 1 / LCD data bit 4 / LCD data bit 8 / GPIO 28
V14	gpio[29]	gpio[29]	pull up	4	I/O	BD4CUIF	Timer 5 / LCD data bit 5 / LCD data bit 9 / GPIO 29
W15	gpio[30]	gpio[30]	pull up	4	I/O	BD4CUIF	Timer 6 / LCD data bit 6 / LCD data bit 10 / GPIO 30
Y16	gpio[31]	gpio[31]	pull up	4	I/O	BD4CUIF	Timer 7 / LCD data bit 7 / LCD data bit 11 / GPIO 31
V15	gpio[32]	gpio[32]	pull up	4	I/O	BD4CUIF	Ext IRQ 2 / 1284 data bit 0 / LCD data bit 8 / GPIO 32
W16	gpio[33]	gpio[33]	pull up	4	I/O	BD4CUIF	Timer 8 / 1284 data bit 1 / LCD data bit 9 / GPIO 33
Y17	gpio[34]	gpio[34]	pull up	4	I/O	BD4CUIF	Timer 9 / 1284 data bit 2 / LCD data bit 10 / GPIO 34

U15	gpio[35]	gpio[35]	pull up	4	I/O	BD4CUIF	Timer 10 / 1284 data bit 3 / LCD data bit 11 / GPIO 35
V16	gpio[36]	gpio[36]	pull up	4	I/O	BD4CUIF	reserved / 1284 data bit 4 / LCD data bit 12 / GPIO 36
W17	gpio[37]	gpio[37]	pull up	4	I/O	BD4CUIF	reserved / 1284 data bit 5 / LCD data bit 13 / GPIO 37
Y18	gpio[38]	gpio[38]	pull up	4	I/O	BD4CUIF	reserved / 1284 data bit 6 / LCD data bit 14 / GPIO 38
U16	gpio[39]	gpio[39]	pull up	4	I/O	BD4CUIF	reserved / 1284 data bit 7 / LCD data bit 15 / GPIO 39
V17	gpio[40]	gpio[40]	pull up	4	I/O	BD4CUIF	Ser port C TxData / Ext IRQ 3 / LCD data bit 16 / GPIO 40
W18	gpio[41]	gpio[41]	pull up	4	I/O	BD4CUIF	Ser port C RxData / reserved / LCD data bit 17 / GPIO 41
U18	gpio[42]	gpio[42]	pull up	2	I/O	BD2CUIF	Ser port C RTS / reserved / USB phy data + / GPIO 42
V20	gpio[43]	gpio[43]	pull up	2	I/O	BD2CUIF	Ser port C CTS / reserved / USB phy data - / GPIO 43
U19	gpio[44]	gpio[44]	pull up	2	I/O	BD2CUIF	Ser port D TxData / 1284 periph on line / USB phy tx oe / GPIO 44
U20	gpio[45]	gpio[45]	pull up	2	I/O	BD2CUIF	Ser port D RxData / 1284 data strobe / USB phy rx data / GPIO 45
T19	gpio[46]	gpio[46]	pull up	2	I/O	BD2CUIF	Ser port D RTS / 1284 auto line feed / USB phy rx data + / GPIO 46
R18	gpio[47]	gpio[47]	pull up	2	I/O	BD2CUIF	Ser port D CTS / 1284 init / USB phy rx data - / GPIO 47
T20	gpio[48]	gpio[48]	pull up	2	I/O	BD2CUIF	USB phy suspend / 1284 periph select / DMA ch 2 req / GPIO 48
R19	gpio[49]	gpio[49]	pull up	2	I/O	BD2CUIF	USB phy speed / 1284 periph logic / DMA ch 2 done / GPIO 49
K2	gpio[50]	gpio[50]		2	I/O	BD2CIF	MII mng data / USB phy data + / reserved / GPIO 50
U3	gpio[51]	gpio[51]		2	I/O	BD2CIF	MII rx data valid / USB phy data - / reserved / GPIO 51
V1	gpio[52]	gpio[52]		2	I/O	BD2CIF	MII rx error / USB phy tx oe / reserved / GPIO 52
N3	gpio[53]	gpio[53]		2	I/O	BD2CIF	MII rx data 0 / USB phy rx data / reserved / GPIO 53
N2	gpio[54]	gpio[54]		2	I/O	BD2CIF	MII rx data 1 / USB phy suspend / reserved / GPIO 54
N1	gpio[55]	gpio[55]		2	I/O	BD2CIF	MII rx data 2 / USB phy speed / reserved / GPIO 55
M3	gpio[56]	gpio[56]		2	I/O	BD2CIF	MII rx data 3 / USB phy rx data + / reserved / GPIO 56
M2	gpio[57]	gpio[57]		2	I/O	BD2CIF	MII tx en / USB phy rx data - / reserved / GPIO 57
M1	gpio[58]	gpio[58]		2	I/O	BD2CIF	MII tx error / reserved / reserved / GPIO 58
L3	gpio[59]	gpio[59]		2	I/O	BD2CIF	MII tx data 0 / reserved / reserved / GPIO 59
L1	gpio[60]	gpio[60]		2	I/O	BD2CIF	MII tx data 1 / reserved / reserved / GPIO 60
K1	gpio[61]	gpio[61]		2	I/O	BD2CIF	MII tx data 2 / reserved / reserved / GPIO 61
K3	gpio[62]	gpio[62]		2	I/O	BD2CIF	MII tx data 3 / reserved / reserved / GPIO 62
P2	gpio[63]	gpio[63]		2	I/O	BD2CIF	MII collision / reserved / reserved / GPIO 63
R1	gpio[64]	gpio[64]		2	I/O	BD2CIF	MII carrier sense / reserved / reserved / GPIO 64
P1	gpio[65]	gpio[65]		2	I/O	BD2CIF	MII phy int / reserved / reserved / GPIO 65
H19	gpio[66]	gpio[66]		8	I/O	BD8RCIF	mem addr[22] / reserved / reserved / GPIO 66
E18	gpio[67]	gpio[67]		8	I/O	BD8RCIF	mem addr[23] / reserved / reserved / GPIO 67
D19	gpio[68]	gpio[68]		8	I/O	BD8RCIF	mem addr[24] / mem clk en [0] / Ext IRQ 0 / GPIO 68
C20	gpio[69]	gpio[69]		8	I/O	BD8RCIF	mem addr[25] / mem clk en [1] / Ext IRQ 1 / GPIO 69
A17	gpio[70]	gpio[70]		8	I/O	BD8RCIF	mem addr[26] / mem clk en [2] / iic scl / GPIO 70
B16	gpio[71]	gpio[71]		8	I/O	BD8RCIF	mem addr[27] / mem clk en [3] / iic sda / GPIO 71

J1	gpio[72]	gpio[72]	8	I/O	BD8RCIF	mem ta strb / reserved / reserved / GPIO 72
USB Interface						
Pin Number	**BSDL Name**	**Signal Name**	**OD (mA)**	**I/O**	**Buf Type**	**Description**
B17	usb_dm	usb_dm		I/O	USB11C1	USB data -
A18	usb_dp	usb_dp		I/O	USB11C1	USB data +
System Memory Interface						
Pin Number	**BSDL Name**	**Signal Name**	**OD (mA)**	**I/O**	**Buf Type**	**Description**
P18	mpmcaddrout[0]	addr[0]	8	O	BT8RIF	address bus signals
R20	mpmcaddrout[1]	addr[1]	8	O	BT8RIF	.
P19	mpmcaddrout[2]	addr[2]	8	O	BT8RIF	.
P20	mpmcaddrout[3]	addr[3]	8	O	BT8RIF	.
N18	mpmcaddrout[4]	addr[4]	8	O	BT8RIF	.
N19	mpmcaddrout[5]	addr[5]	8	O	BT8RIF	.
N20	mpmcaddrout[6]	addr[6]	8	O	BT8RIF	.
M18	mpmcaddrout[7]	addr[7]	8	O	BT8RIF	.
M19	mpmcaddrout[8]	addr[8]	8	O	BT8RIF	.
M20	mpmcaddrout[9]	addr[9]	8	O	BT8RIF	.
L19	mpmcaddrout[10]	addr[10]	8	O	BT8RIF	.
L18	mpmcaddrout[11]	addr[11]	8	O	BT8RIF	.
L20	mpmcaddrout[12]	addr[12]	8	O	BT8RIF	.
K20	mpmcaddrout[13]	addr[13]	8	O	BT8RIF	.
K18	mpmcaddrout[14]	addr[14]	8	O	BT8RIF	.
K19	mpmcaddrout[15]	addr[15]	8	O	BT8RIF	.
J20	mpmcaddrout[16]	addr[16]	8	O	BT8RIF	.
J19	mpmcaddrout[17]	addr[17]	8	O	BT8RIF	.
J18	mpmcaddrout[18]	addr[18]	8	O	BT8RIF	.
H20	mpmcaddrout[19]	addr[19]	8	O	BT8RIF	.
H18	mpmcaddrout[20]	addr[20]	8	O	BT8RIF	.
G20	mpmcaddrout[21]	addr[21]	8	O	BT8RIF	.
C15	mpmcclkout[0]	clk_out[0]	8	O	BT8RIF	SDRAM clocks
A12	mpmcclkout[1]	clk_out[1]	8	O	BT8RIF	.
A7	mpmcclkout[2]	clk_out[2]	8	O	BT8RIF	.
G1	mpmcclkout[3]	clk_out[3]	8	O	BT8RIF	.
A16	mpmcdataio[0]	data[0]	8	I/O	BD8RCIF	data bus signals
B15	mpmcdataio[1]	data[1]	8	I/O	BD8RCIF	.
C14	mpmcdataio[2]	data[2]	8	I/O	BD8RCIF	.
A15	mpmcdataio[3]	data[3]	8	I/O	BD8RCIF	.

B14	mpmcdataio[4]	data[4]	8	I/O	BD8RCIF	.
A14	mpmcdataio[5]	data[5]	8	I/O	BD8RCIF	.
C13	mpmcdataio[6]	data[6]	8	I/O	BD8RCIF	.
B13	mpmcdataio[7]	data[7]	8	I/O	BD8RCIF	.
A13	mpmcdataio[8]	data[8]	8	I/O	BD8RCIF	.
C12	mpmcdataio[9]	data[9]	8	I/O	BD8RCIF	.
B12	mpmcdataio[10]	data[10]	8	I/O	BD8RCIF	.
B11	mpmcdataio[11]	data[11]	8	I/O	BD8RCIF	.
C11	mpmcdataio[12]	data[12]	8	I/O	BD8RCIF	.
A11	mpmcdataio[13]	data[13]	8	I/O	BD8RCIF	.
A10	mpmcdataio[14]	data[14]	8	I/O	BD8RCIF	.
C10	mpmcdataio[15]	data[15]	8	I/O	BD8RCIF	.
B10	mpmcdataio[16]	data[16]	8	I/O	BD8RCIF	.
A9	mpmcdataio[17]	data[17]	8	I/O	BD8RCIF	.
B9	mpmcdataio[18]	data[18]	8	I/O	BD8RCIF	.
C9	mpmcdataio[19]	data[19]	8	I/O	BD8RCIF	.
A8	mpmcdataio[20]	data[20]	8	I/O	BD8RCIF	.
B8	mpmcdataio[21]	data[21]	8	I/O	BD8RCIF	.
C8	mpmcdataio[22]	data[22]	8	I/O	BD8RCIF	.
B7	mpmcdataio[23]	data[23]	8	I/O	BD8RCIF	.
A6	mpmcdataio[24]	data[24]	8	I/O	BD8RCIF	.
C7	mpmcdataio[25]	data[25]	8	I/O	BD8RCIF	.
B6	mpmcdataio[26]	data[26]	8	I/O	BD8RCIF	.
A5	mpmcdataio[27]	data[27]	8	I/O	BD8RCIF	.
C6	mpmcdataio[28]	data[28]	8	I/O	BD8RCIF	.
B5	mpmcdataio[29]	data[29]	8	I/O	BD8RCIF	.
A4	mpmcdataio[30]	data[30]	8	I/O	BD8RCIF	.
C5	mpmcdataio[31]	data[31]	8	I/O	BD8RCIF	.
F2	mpmcdqmout[0]	data_mask[0]	8	O	BT8RIF	SDRAM data mask signals
G3	mpmcdqmout[1]	data_mask[1]	8	O	BT8RIF	.
F1	mpmcdqmout[2]	data_mask[2]	8	O	BT8RIF	.
G2	mpmcdqmout[3]	data_mask[3]	8	O	BT8RIF	.
J2	mpmcfbclkin_0	clk_in		I	IBUFIF	SDRAM feed-back clocks
D1	nmpmcblsout[0]	byte_lane_sel_n[0]	8	O	BT8RIF	Static memory byte lane select signals
E2	nmpmcblsout[1]	byte_lane_sel_n[1]	8	O	BT8RIF	.
F3	nmpmcblsout[2]	byte_lane_sel_n[2]	8	O	BT8RIF	.
E1	nmpmcblsout[3]	byte_lane_sel_n[3]	8	O	BT8RIF	.

H1	nmpmccasout	cas_n	8	O	BT8RIF	SDRAM column address strobe
B4	nmpmcdycsout[0]	dy_cs_n[0]	8	O	BT8RIF	SDRAM chip select signals
A3	nmpmcdycsout[1]	dy_cs_n[1]	8	O	BT8RIF	.
D5	nmpmcdycsout[2]	dy_cs_n[2]	8	O	BT8RIF	.
C4	nmpmcdycsout[3]	dy_cs_n[3]	8	O	BT8RIF	.
H2	nmpmcoeout	st_oe_n	8	O	BT8RIF	Static memory output enable
J3	nmpmcrasout	ras_n	8	O	BT8RIF	SDRAM row address strobe
B3	nmpmcstcsout[0]	st_cs_n[0]	8	O	BT8RIF	Static memory chip select signals
C1	nmpmcstcsout[1]	st_cs_n[1]	8	O	BT8RIF	.
D2	nmpmcstcsout[2]	st_cs_n[2]	8	O	BT8RIF	.
E3	nmpmcstcsout[3]	st_cs_n[3]	8	O	BT8RIF	.
H3	nmpmcweout	we_n	8	O	BT8RIF	SDRAM write enable

Ethernet Interface (remainder of Ethernet signals are behind GPIO)

Pin Number	BSDL Name	Signal Name	OD (mA)	I/O	Buf Type	Description
L2	mdc	mdc	4	O	BT4IF	MII clock
V4	rx_clk	rx_clk		I	IBUFIF	Receive clock
V2	tx_clk	tx_clk		I	IBUFIF	Transmit clock

Clock Generation/System Pins

Pin Number	BSDL Name	Signal Name		OD (mA)	I/O	Buf Type	Description
R2	x1_sys_osc	x1_sys_osc			I	OSCI	System clock crystal oscillator circuit input
P3	x2_sys_osc	x2_sys_osc			O	OSCL40C10	System clock crystal oscillator circuit output
T1	sys_osc_vdd	sys_osc_vdd					System oscillator 3.3V
F18	x1_usb_osc	x1_usb_osc			I	OSCI	USB clock crystal oscillator circuit input
E20	x2_usb_osc	x2_usb_osc			O	OSCL60C10	USB clock crystal oscillator circuit output
E19	usb_osc_vdd	usb_osc_vdd					USB oscillator 3.3V
W4	reset_done_io	reset_done	pull up	2	I/O	BD2CUIF	System reset done signal, CPU enabled when asserted
U5	reset_n	reset_n	pull up		I	IBUFUIF	System reset input signal
W3	sreset_n	sreset_n	pull up		I	IBUFUIF	System soft reset input signal
V5	bist_en_in_n	bist_en_n			I	IBUFIF	Enable internal bist operation
U6	pll_test_in_n	pll_test_n			I	IBUFIF	Enable PLL testing
Y3	scan_en_in_n	scan_en_n			I	IBUFIF	Enable internal scan testing
R3	sys_pll_dvdd	sys_pll_dvdd				ZSAPLVDCR	System clock PLL 1.5V digital power
T2	sys_pll_dvss	sys_pll_dvss				ZSAPLVSCR	System clock PLL digital ground
U2	sys_pll_avdd	sys_pll_avdd				ZSAPLVDRR	System clock PLL 3.3V analog power
U1	sys_pll_avss	sys_pll_avss				ZSAPLVSLR	System clock PLL analog ground
T3	pll_lpf	pll_lpf			O	ZSAOUTNCR	System PLL Manufacturing Test Pin

JTAG Interface for ARM Core / Boundary Scan

Pin Number	BSDL Name	Signal Name		OD (mA)	I/O	Buf Type	Description
G18	tck_in	tck			I	IBUFIF	Test clock
D20	tdi_in	tdi	pull up		I	IBUFUIF	Test data in
G19	tdo_out	tdo		2	O	BT2IF	Test data out
F19	tms_in	tms	pull up		I	IBUFUIF	Test mode select
F20	trst_in_n	trst_n	pull up		I	IBUFUIF	Test mode reset
Y4	rtck_out	rtck	pull up	2	I/O	BD2CUIF	Returned test clock, ARM core only

Power/Ground

Pin Number	BSDL Name	Signal Name	Description
K4	n/a	VDDC	Core power, 1.5V
L4	n/a	VDDC	.
M4	n/a	VDDC	.
U10	n/a	VDDC	.
U11	n/a	VDDC	.
J17	n/a	VDDC	.
K17	n/a	VDDC	.
L17	n/a	VDDC	.
D11	n/a	VDDC	.
D10	n/a	VDDC	.
F4	n/a	VDDS	I/O power, 3.3V
G4	n/a	VDDS	.
P4	n/a	VDDS	.
R4	n/a	VDDS	.
U8	n/a	VDDS	.
U7	n/a	VDDS	.
U13	n/a	VDDS	.
U14	n/a	VDDS	.
P17	n/a	VDDS	.
R17	n/a	VDDS	.
F17	n/a	VDDS	.
G17	n/a	VDDS	.
D15	n/a	VDDS	.
D14	n/a	VDDS	.
D7	n/a	VDDS	.
D6	n/a	VDDS	.
A2	n/a	VSS	Ground
A1	n/a	VSS	.

B2	n/a	VSS	.
B1	n/a	VSS	.
C3	n/a	VSS	.
D4	n/a	VSS	.
W1	n/a	VSS	.
Y1	n/a	VSS	.
W2	n/a	VSS	.
Y2	n/a	VSS	.
V3	n/a	VSS	.
U4	n/a	VSS	.
Y19	n/a	VSS	.
Y20	n/a	VSS	.
W19	n/a	VSS	.
W20	n/a	VSS	.
V18	n/a	VSS	.
U17	n/a	VSS	.
B20	n/a	VSS	.
A20	n/a	VSS	.
B19	n/a	VSS	.
A19	n/a	VSS	.
C18	n/a	VSS	.
D17	n/a	VSS	.
E4	n/a	VSS	.
H4	n/a	VSS	.
J4	n/a	VSS	.
C16	n/a	VSS	.
H17	n/a	VSS	.
E17	n/a	VSS	.
M17	n/a	VSS	.
N17	n/a	VSS	.
T17	n/a	VSS	.
D13	n/a	VSS	.
D12	n/a	VSS	.
D9	n/a	VSS	.
D8	n/a	VSS	.
U12	n/a	VSS	.
U9	n/a	VSS	.

T4	n/a	VSS					.
N4	n/a	VSS					.
C2	n/a	VSS					.
D3	n/a	VSS					.
D16	n/a	VSS					.
C17	n/a	VSS					.
D18	n/a	VSS					.
B18	n/a	VSS					.
C19	n/a	VSS					.
V19	n/a	VSS					.
J9	n/a	VSS					.
J10	n/a	VSS					.
J11	n/a	VSS					.
J12	n/a	VSS					.
K9	n/a	VSS					.
K10	n/a	VSS					.
K11	n/a	VSS					.
K12	n/a	VSS					.
L9	n/a	VSS					.
L10	n/a	VSS					.
L11	n/a	VSS					.
L12	n/a	VSS					.
M9	n/a	VSS					.
M10	n/a	VSS					.
M11	n/a	VSS					.
M12	n/a	VSS					.

Reserved/No Connects

Pin Number		Signal Name	Pull Up/Down	OD (mA)	I/O	Buf Type	Description
T18	n/a	reserved					no connect