

## SERIAL INTERNAL/EXTERNAL INTERFACE TIMING CHARACTERISTICS

Operating conditions:

TEMPERATURE: -15.00 (min) 110.00 (max)

VOLTAGE : 1.60 (min) 1.40 (max)

OUTPUT LOAD : 25.0pf

INPUT DRIVE : CMOS BUFFER

\* The Tsys parameter represents one period of the internal system clock.

Num	Characteristic	Min	Max	Unit
76	SCLK to ENABLE high	1		Tsclk
77	SCLK to TXD (PORTA7/C7)		1Tsys*	ns
78	RXD (PORTA3/C3) setup to SCLK	1		ns
79	RXD hold to SCLK	1		ns

Figure 1. Serial Internal Interface Timing Characteristics

Num	Characteristic	Min	Max	Unit
80	SCLK frequency		10	MHz
	SCLK duty cycle	45	55	%
81	SCLK to ENABLE	1		Tsclk
82	SCLK to TXD (PORTA7/C7)		2Tsys*	ns
83	RXD (PORTA3/C3) setup to SCLK	2		ns
84	RXD hold to SCLK	1.5		ns

Figure 2. Serial External Interface Timing Characteristics