

Digi TransPort® Family

Serial Pin-outs



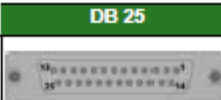

Depending upon the model, the asynchronous serial ports on may be presented as DB 25, DB 9 or 8-pin RJ45 sockets. On some models, a combination of the above may be used. The following tables list the pin designations of each type of connector for each Digi Transport model. The RS-232 port pin-outs are suitable for both Async and Sync port connections. When used in Async mode the pins for Tx_C, Rx_C & ETC are not required, these are needed for Sync mode only. **Please note that the Transport's serial ports are DCE.**

For Digi models that include additional serial ports which have RJ45 connectors. Digi offers the following serial cables for connectivity:

- P/N: 76000855 -- RJ45 to DB9 Female - 6'
- P/N: 76000856 -- RJ45 to DB25 Male - 6'
- P/N: 76000857 -- RJ45 to DB25 Female - 6'

DR6410, DR6420, DR6460, DR64x0W & WR41

RS-232 Port Pin-Outs

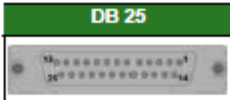
			DB 25	RJ45
				
Description	RS232 signal	Direction ¹	Pin #	Pin #
Transmit Data	TxD	in	2	6
Receive Data	RxD	out	3	3
Ready To Send	RTS	in	4	1
Clear To Send	CTS	out	5	8
Data Set Ready	DSR	out	6	n/a
Ground	GND	n/a	7	5
Data Carrier Detect	DCD	out	8	7
Transmitter Clock	TxC	out	15	n/a
Receiver Clock	RxC	out	17	n/a
Data Terminal Ready	DTR	in	20	2
Ring Indicate	RI	out	22	n/a
External Transmitter Clock	ETC	in	24	n/a

1. With respect to Digi units

X.21 (RS-422)

Note:

In order for the DR64x0(W) to operate in X.21 mode, a kepler daughter card must be fitted.




			DB 25
			
Description	X.21 signal	Direction ¹	Pin #
Receive Data (A)	RxDA	out	3
Receive Data (B)	RxDB	out	16
Transmit Data (A)	TxDA	in	2
Transmit Data (B)	TxDB	in	14
Indication (B)	INDB	out	13
Ground	GND	n/a	7
Control (B)	CTLB	in	19
Clock (A)	CLKA	in or out ²	17
Clock (B)	CLKB	in or out ²	9
Indication (A)	INDA	out	5
Control (A)	CTLA	in	4

1. With respect to Digi units

2. Direction depends on whether the Digi unit is clock sink or clock source.

WR44

RS-232 Port Pin-Outs


			DB 25	DB 9	RJ45
					
Description	RS232 signal	Direction ¹	Pin #	Pin #	Pin #
Transmit Data	TxD	in	2	3	6
Receive Data	RxD	out	3	2	3
Ready To Send	RTS	in	4	7	1
Clear To Send	CTS	out	5	8	8
Data Set Ready	DSR	out	6	6	n/a
Ground	GND	n/a	7	5	5
Data Carrier Detect	DCD	out	8	1	7
Transmitter Clock	TxC	out	15	n/a	n/a
Receiver Clock	RxC	out	17	n/a	n/a
Data Terminal Ready	DTR	in	20	4	2
Ring Indicate	RI	out	22	9	n/a
External Transmitter Clock	ETC	in	24	n/a	n/a

1. With respect to Digi units

X.21 (RS-422)

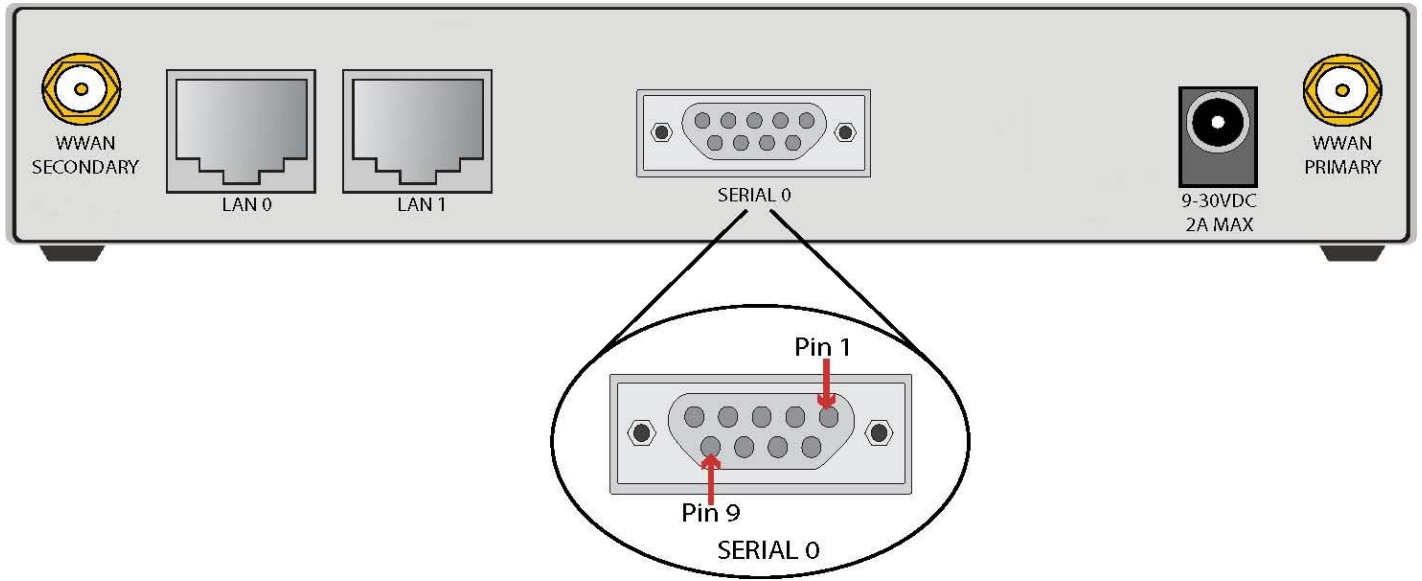
Note:

In order for the WR44 to operate in X.21 mode, a Viper daughter card must be fitted.

				DB 25
				
Description	X.21 signal	Direction ¹	Pin #	
Transmit Data (A)	TxDA	in	2	
Receive Data (A)	RxDA	out	3	
Control (A)	CTLA	in	4	
Indication (A)	INDA	out	5	
Ground	GND	n/a	7	
Clock (B)	CLKB	in or out ²	9	
Indication (B)	INDB	out	13	
Transmit Data (B)	TxDB	in	14	
Receive Data (B)	RxDB	out	16	
Clock (A)	CLKA	in or out ²	17	
Control (B)	CTLB	in	19	

1. With respect to Digi units

2. Direction depends on whether the Digi unit is clock sink or clock source.



Pin #	Direction	RS232 DCE	Description
1	Out	DCD	Data Carrier Detect
2	Out	RXD	Receive Data
3	In	TXD	Transmit Data
4	In	DTR	Data Terminal Ready
5	N/A	GND	Ground
6	Out	DSR	Data Set Ready
7	In	RTS	Ready To Send
8	Out	CTS	Clear To Send
9	Out	RI	Ring Indicate

Pin #	Direction	RS422/RS485	Description
1	Out	CTS_B	Clear To Send B
2	Out	RD_A	Receive Data A
3	In	TD_A	Transmit Data A
4	In	RTS_B	Ready To Send B
5	N/A	GND	Ground
6	Out	RD_B	Receive Data B
7	In	RTS_A	Ready To Send A
8	Out	CTS_A	Clear To Send A
9	In	TD_B	Transmit B

Note: If RS485 2-wire half-duplex mode is required instead of the 4-wire configuration then TD_A and RD_A pair and TD_B and RD_B pair should be connected together.

Digi TransPort® WR Family

3G/4G enterprise-class cellular routers



For additional support go to our support site <http://www.digi.com/support>

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